

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number		10001478	
	Filing Date		2001-11-01	
	First Named Inventor		Craig Nemecek	
	Art Unit		2123	
	Examiner Name		Jason Scott Proctor	
	Attorney Docket Number		CD01213M	

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Application Number	10001478
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Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

1	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al., filed on 09/26/2001; 25 pages	<input type="checkbox"/>
2	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Monte Mar, filed on 04/25/2001; 28 pages	<input type="checkbox"/>

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1	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Monte Mar, filed on 08/29/2001; 46 pages	<input type="checkbox"/>
2	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Perrin et al., filed on 09/09/2002; 41 pages	<input type="checkbox"/>
3	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Monte Mar, filed on 12/09/1998; 23 pages	<input type="checkbox"/>

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	1	20040054821	A1	2004-03-18	Warren et al.	Entire Document

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	1	9532478	WO	A1	1995-11-30	Klien	Entire Document	<input type="checkbox"/>
	2	404083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>
	3	404095408	JP	A1	2002-03-27	Nishimaki	Entire Document	<input type="checkbox"/>

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	4	405055842	JP	A1	1993-03-26	Kondo	Entire Document	<input type="checkbox"/>
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	1	The Provisional Application 60/243,708 (CD00167P) "Advanced Programmable Microcontroller Device"; Snyder et al., filed on 10/26/2000; 277 pages	<input type="checkbox"/>
	2	USPTO Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability," Mar et al., filed on 09/19/2001; 28 pages	<input type="checkbox"/>
	3	USPTO Application Number 09/969,313 (CD00227): "Architecture for Synchronizing and Resetting Clock Signals Supplied to Multiple Analog Programmable Analog Blocks," Bert Sullam, filed on 10/01/2001; 50 pages	<input type="checkbox"/>
	4	USPTO Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode," Warren Snyder, filed on 10/05/2001; 31 pages	<input type="checkbox"/>
	5	USPTO Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface," Warren Snyder, filed on 10/05/2001; 32 pages	<input type="checkbox"/>
	6	USPTO Application Number 09/972,133 (CD00178): "Method for Entering Circuit Test Mode," Warren Snyder, filed on 10/05/2001; 30 pages	<input type="checkbox"/>
	7	USPTO Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm," Warren Snyder, filed on 10/09/2001; 26 pages	<input type="checkbox"/>
	8	USPTO Application Number 09/977,111 (CD00202): "A Frequency Doubler Circuit with Trimmable Current Control," Shutt et al., filed on 10/11/2001; 35 pages	<input type="checkbox"/>

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9	USPTO Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture," Warren Snyder, filed on 10/15/2002; 36 pages	<input type="checkbox"/>
10	USPTO Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 05/09/2005; 1 page	<input type="checkbox"/>
11	USPTO Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks," Warren Snyder, filed on 05/14/2001; 28 pages	<input type="checkbox"/>
12	USPTO Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller," Kutz et al., filed on 06/22/2001; 44 pages	<input type="checkbox"/>
13	USPTO Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller," Sullam et al., filed on 10/24/2001; 34 pages	<input type="checkbox"/>
14	USPTO Application Number 10/001,477 (CD01203M): "Breakpoint Control in an In-Circuit Emulation System," Roe et al., filed on 11/01/2001; 43 pages	<input type="checkbox"/>
15	USPTO Application Number 10/004,197 (CD01220M): "In-Circuit Emulator with Gatekeeper Based Halt Control," Nemecek et al., filed on 11/14/2001; 47 pages	<input type="checkbox"/>
16	USPTO Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer," Nemecek et al., filed on 11/14/2001; 46 pages	<input type="checkbox"/>
17	USPTO Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System," Craig Nemecek, filed on 11/01/2001; 43 pages	<input type="checkbox"/>
18	USPTO Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer," Nemecek et al., filed on 11/01/2001; 47 pages	<input type="checkbox"/>
19	USPTO Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies," Sullam et al., filed on 10/25/2001; 49 pages	<input type="checkbox"/>

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20	USPTO Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller," Kutz et al., filed on 06/22/2001; 42 pages	<input type="checkbox"/>
21	USPTO Application Number 09/826,397 (CD00190): "Method and Circuit for Allowing a Microprocessor to Change its Operating Frequency on-the-Fly," Bert Sullam, filed on 04/02/2001; 24 pages	<input type="checkbox"/>
22	USPTO Application Number 09/893,048 (CD00231): "A Microcontroller having an On-Chip High Gain Amplifier," Kutz et al., filed on 06/26/2001; 22 pages	<input type="checkbox"/>
23	USPTO Application Number 09/912,768 (CD00200): "A Microcontroller having a Dual Mode Relax Oscillator that is Trimmable," James Shutt; filed on 07/24/2001; 33 pages	<input type="checkbox"/>
24	USPTO Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller," Kutz et al., filed on 08/03/2001; 38 pages	<input type="checkbox"/>
25	USPTO Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 08/03/2001; 37 pages	<input type="checkbox"/>
26	USPTO Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit," Warren Snyder, filed on 08/06/2001; 25 pages	<input type="checkbox"/>
27	USPTO Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit," Monte Mar, filed on 08/22/2001; 51 pages	<input type="checkbox"/>

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	2	6094730	B1	2000-07-25	Lopez et al.	Entire Document
	3	6107826	B1	2000-08-22	Young et al.	Entire Document
	4	6161199	B1	2000-12-12	Szeto et al.	Entire Document
	5	6173419	B1	2001-01-09	Barnett	Entire Document
	6	6185522	B1	2001-02-06	Bakker	Entire Document
	7	6223144	B1	2001-04-24	Barnett et al.	Entire Document
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13	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
14	5357626	B1	1994-10-18	Johnson et al.	Entire Document
15	5493723	B1	1996-02-20	Beck et al.	Entire Document
16	5546562	B1	1996-08-13	Patel	Entire Document
17	5559996	B1	1996-09-24	Fujioka et al.	Entire Document
18	5572665	B1	1996-11-05	Nakabayashi et al.	Entire Document
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	34	6009270	B1	1999-12-28	Mann	Entire Document
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	36	6034538	B1	2000-03-07	Abramovici	Entire Document
	37	6058263	B1	2000-05-02	Voth	Entire Document
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	39	6289300	B1	2001-09-11	Brannick et al.	Entire Document
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	42	6347395	B1	2002-02-12	Payne et al.	Entire Document
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	44	6542844	B1	2003-04-01	Hanna	Entire Document

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	1	20030149961	A1	2003-08-07	Kawai et al.	Entire Document
	2	20030056071	A1	2003-03-20	Triece et al.	Entire Document
	3	20020052729	A1	2002-05-02	Kyung et al.	Entire Document
	4	20020116168	A1	2002-08-22	Kim, Mahn Bok	Entire Document
	5	20020156885	A1	2002-10-24	Thakkar, Bina Kunal	Entire Document
	6	20020174134	A1	2002-11-21	Goykhman	Entire Document

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	1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	<input type="checkbox"/>
	2	ANONYMOUS, "Warp Nine Engineering - The IEEE 1284 Experts-F/Port Product Sheet," undated web page found at http://www.fapo.com/fport.htm ; printed on 4/12/2005; 2 pages	<input type="checkbox"/>
	3	ANONYMOUS, "F/Port:Fast Parallel Port for the PC" Installation Manual" Release 7.1, circa 1997, available for download from http://www.fapo.com/fport.htm ; 25 pages	<input type="checkbox"/>
	4	USPTO Final Rejection for 09/989,765 (CD01194M) dated 03/31/09; 18 pages	<input type="checkbox"/>
	5	USPTO Final Rejection for 09/989,778 (CD01172) dated 03/16/09; 26 pages	<input type="checkbox"/>

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	2	6223147	B1	2001-04-24	Bowers	Entire Document
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	5	6408432	B1	2002-06-18	Herrmann, et al.	Entire Document
	6	6411974	B1	2002-06-25	Graham, et al.	Entire Document
	7	6449755	B1	2002-09-10	Beausang, et al.	Entire Document
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9	6769622	B1	2004-08-03	Tournemille, et al.	Entire Document
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11	6169383	B1	2001-01-02	Sabin et al.	Entire Document
12	6611856	B1	2003-08-26	Liao et al.	Entire Document
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14	5519854	B1	1996-05-21	Watt	Entire Document
15	7024654	B1	2006-04-04	Bersch et al.	Entire Document
16	7376904	B1	2008-05-20	Cifra et al.	Entire Document
17	7266768	B1	2007-09-04	Ferlitsch et al.	Entire Document
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	1	USPTO Non-Final Rejection for 09/998,848 (CD01177M) dated 12/21/04; 14 pages	<input type="checkbox"/>
	2	USPTO Final Rejection for 09/989,767 (CD01178M) dated 01/11/07; 12 pages	<input type="checkbox"/>

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3	"In-Circuit Emulators - descriptions of the major ICES around"; retrieved on November 14, 2005 from http://www.algonet.se/~staffann/developer/emulator.htm ; 6 pages	<input type="checkbox"/>
4	USPTO Application Number 09/975,104 (CD00183): "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation," Warren Snyder, filed on 10/10/2001; 35 pages	<input type="checkbox"/>
5	USPTO Final Rejection for 09/989,767 (CD01178M) dated 01/15/09; 21 pages	<input type="checkbox"/>
6	USPTO Final Rejection for 09/989,767 (CD01178M) dated 03/06/06; 15 pages	<input type="checkbox"/>
7	USPTO Final Rejection for 09/989,767 (CD01178M) dated 04/06/05; 13 pages	<input type="checkbox"/>
8	USPTO Final Rejection for 09/989,767 (CD01178M) dated 12/27/07; 21 pages	<input type="checkbox"/>
9	USPTO Application Number 09/975,105 (CD00184): "Host to FPGA Interface in an In-Circuit Emulation System," Craig Nemecek, filed on 10/10/2001; 44 pages	<input type="checkbox"/>
10	Bauer et al.; "A Reconfigurable Logic Machine for Fast Event-Driven Simulation"; June 1998; Design Automation Conference Proceedings; 8 pages	<input type="checkbox"/>
11	USPTO Application Number 09/975,338 (CD00186): "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events," Nemecek et al., filed on 10/10/2001; 34 pages	<input type="checkbox"/>
12	USPTO Application Number 09/975,030 (CD00185): "Emulator Chip-Board Architecture for Interface," Snyder et al., filed on 10/10/2001; 37 pages	<input type="checkbox"/>
13	Wikipedia - Main Page, retrieved on March 8, 2006 from http://en.wikipedia.org/wiki/Main_Page and http://en.wikipedia.org/wiki/Wikipedia:Introduction ; 5 pages	<input type="checkbox"/>

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14	Wikipedia - Processor register, retrieved on March 7, 2006 from http://en.wikipedia.org/wiki/Processor_register ; 4 pages	<input type="checkbox"/>
15	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 259 pages	<input type="checkbox"/>
16	Dahl, et al.; "Emulation of the Sparcle Microprocessor with the MIT Virtual Wires Emulation System"; 1994; IEEE; 9 pages	<input type="checkbox"/>

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1	USPTO Application Number 10/033,027 (CD00232M): "Microcontrollable Programmable System on a Chip," Warren Snyder; filed on 10/22/2001; 117 pages	<input type="checkbox"/>
2	USPTO Application Number 10/803,030 (CD00169C): "Programmable Microcontrollable Architecture (Mixed Analog/Digital)," Snyder et al., filed on 03/16/2004; 13 pages	<input type="checkbox"/>

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1	Wikipedia "XML" retrieved on January 29, 2007 from http://en.wikipedia.org/wiki/XML ; 16 pages	<input type="checkbox"/>
2	Cypress MicroSystem, Inc. "Cypress Customer Forums" retrieved from < http://www.cypress.com/forums/messageview >; 11/30/2004; 1 page	<input type="checkbox"/>
3	USPTO Application Number 10/113,064 (CD01200M): "Method and System for Debugging through Supervisory Operating Codes and Self Modifying Codes," Roe et al., filed on 03/29/2002; 36 pages	<input type="checkbox"/>
4	USPTO Application Number 10/002,726 (CD01171M): "Method and Apparatus for Generating Microcontroller Configuration Information," Ogami et al., filed on 10/24/2001; 54 pages	<input type="checkbox"/>
5	USPTO Notice of Allowance for Application Number 10/002,726 (CD01171M) dated 02/06/2007; 7 pages	<input type="checkbox"/>
6	USPTO Non-Final Rejection for Application Number 10/002,726 (CD01171M) dated 08/28/2006; 10 pages	<input type="checkbox"/>
7	USPTO Final Rejection for Application Number 10/002,726 (CD01171M) dated 03/27/2006; 8 pages	<input type="checkbox"/>
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10	USPTO Non-Final Rejection for Application Number 10/002,726 (CD01171M) dated 12/13/2004; 7 pages	<input type="checkbox"/>
11	USPTO Application Number 11/818,005 (CD01171C): "Techniques for Generating Microcontroller Configuration Information," Ogami et al., filed on 6/12/2007; 61 pages	<input type="checkbox"/>

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1	USPTO Notice of Allowance for Application Number 10/011,214 (CD00191) dated 04/11/2005; 4 pages	<input type="checkbox"/>
2	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," 1999, IEEE; 6 pages	<input type="checkbox"/>
3	USPTO Notice of Allowance for Application Number 09/989,762 (CD01175M) dated 03/25/2009; 7 pages	<input type="checkbox"/>
4	USPTO Application Number 09/275,336 (CD98087): "Programmable Oscillator Scheme," Mar et al., filed on 03/24/1999; 25 pages	<input type="checkbox"/>
5	USPTO Application Number 09/721,316 (CD98087C1): "Programmable Oscillator Scheme," Mar et al., filed on 11/22/2000; 26 pages	<input type="checkbox"/>
6	USPTO Application Number 10/324,455 (CD98087C2): "Programmable Oscillator Scheme," Mar et al., filed on 12/20/2002; 23 pages	<input type="checkbox"/>
7	USPTO Application Number 09/989,815 (CD01166M): "A Data Driven Method and System for Monitoring Hardware Resource Usage for Programming an Electric Device," Bartz et al., filed on 11/19/2001; 36 pages	<input type="checkbox"/>
8	USPTO Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress," Craig Nemecek, filed on 11/15/2001; 33 pages	<input type="checkbox"/>
9	USPTO Application Number 09/998,834 (CD01221M): "A System and a Method for Communication between and Ice and a Production Microcontroller while in a Halt State," Craig Nemecek, filed on 11/15/2001; 33 pages	<input type="checkbox"/>
10	USPTO Application Number 10/113,065 (CD01215M): "System and Method for Automatically Matching Components in a Debugging System," Nemecek et al., filed on 03/29/2002; 32 pages	<input type="checkbox"/>
11	USPTO Application Number 09/989,574 (CD01164M): "Method and System for using a Graphics user Interface for Programming an Electronic Device," Bartz et al., filed on 11/19/2001; 43 pages	<input type="checkbox"/>

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USPTO Application Number 09/989,816 (CD01165M): "Datasheet Browsing and Creation with Data-Driven Datasheet Tabs within a Microcontroller Design Tool," Bartz et al., filed on 11/19/2001; 55 pages

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1	USPTO Application Number 11/850,260 (CD2007006): "Circuit and Method for Improving the Accuracy of a Crystal-less Oscillator Having Dual-Frequency Modes," Wright et al., filed on 09/05/2007; 33 pages	<input type="checkbox"/>
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	10	USPTO Application Number 09/981,448 (CD01102): "Oscillator Tuning Method," Lane T. Hauck, filed on 10/17/2001; 28 pages	<input type="checkbox"/>
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	12	USPTO Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL," Dalmia et al., filed on 12/23/1999; 32 pages	<input type="checkbox"/>
	13	USPTO Application Number 09/471,576 (CD99062): "Reference-Free Clock Generation and Data Recovery PLL," Kamal Dalmia, filed on 12/23/1999; 30 pages	<input type="checkbox"/>
	14	USPTO Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al., filed on 02/26/2002; 28 pages	<input type="checkbox"/>
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	16	USPTO Application Number 09/893,161 (CD00094): "Architecture of a PLL with Dynamic Frequency Control on a PLD," Michael T. Moore, filed on 06/27/2001; 32 pages	<input type="checkbox"/>
	17	USPTO Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al., filed on 08/24/2004; 24 pages	<input type="checkbox"/>
	18	USPTO Application Number 09/398,956 (CD99049): "Frequency Acquisition Rate Control in Phase Lock Loop Circuits," Moyal et al., filed on 09/17/1999; 35 pages	<input type="checkbox"/>

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1	USPTO Application Number 11/201,627 (CD04109): "Method and an apparatus to design a processing system using a graphical user interface," Ogami et al., filed on 08/10/2005; 37 pages	<input type="checkbox"/>
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4	USPTO Application Number 11/200,619 (CD04112): "Providing hardware independence to automate code generation of processing device firmware," Snyder et al., filed on 08/10/2005; 41 pages	<input type="checkbox"/>
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	3	"The Gemini Netlist Comparison Project;" < http://www.cs.washington.edu/research/projects/lis/www/gemini/gemini.html > larry@cs.washington.edu; 3/19/2002; 2 pages	<input type="checkbox"/>
	4	USPTO Non-Final Rejection for Application Number 11/965,291 (CD2007130) dated 12/17/2008; 8 pages	<input type="checkbox"/>
	5	Ebling, "Gemini II: A Second Generation Layout Validation Program;" 1988; in proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-88); 4 pages	<input type="checkbox"/>
	6	USPTO Application Number 12/132,527 (CD01175C): "System and Method for Performing Next Placements and Pruning of Disallowed Placements for Programming an Integrated Circuit;" Ogami et al., filed on 06/03/2008; 44 pages	<input type="checkbox"/>
	7	USPTO Application Number 12/356,468 (CD01174D): "System and Method for Dynamically Generating a Configuration Datasheet," Anderson et al.; filed on 01/20/2009; 27 pages	<input type="checkbox"/>
	8	Written Opinion of the International Search Authority for International Application Number PCT/US08/60680 (CD2007130PCT) dated 08/15/2008; 4 pages	<input type="checkbox"/>
	9	USPTO Notice of Allowance for Application Number 09/475,879 (CD99047) dated 10/22/2004; 7 pages	<input type="checkbox"/>
	10	USPTO Advisory Action for Application Number 09/475,879 (CD99047) dated 03/04/2002; 3 pages	<input type="checkbox"/>

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STATEMENT BY APPLICANT**
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Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

11	USPTO Advisory Action for Application Number 09/475,879 (CD99047) dated 12/31/2001; 3 pages	<input type="checkbox"/>
12	USPTO Final Rejection for Application Number 09/475,879 (CD99047) dated 10/11/2001; 10 pages	<input type="checkbox"/>
13	USPTO Non-Final Rejection for Application Number 09/475,879 (CD99047) dated 03/08/2001; 6 pages	<input type="checkbox"/>
14	USPTO Application Number 09/475,879 (CD99047): "Programmable Logic Device," Lacey et al.; filed on 12/30/1999; 50 pages	<input type="checkbox"/>
15	USPTO Application Number 09/475,808 (CD99069): "Configurable Memory for Programmable Logic Circuits," Lacey et al., filed on 12/30/1999; 24 pages	<input type="checkbox"/>
16	USPTO Non-Final Rejection for Application Number 09/475,808 (CD99069) dated 06/06/2001; 8 pages	<input type="checkbox"/>
17	USPTO Notice of Allowance for Application Number 09/475,808 (CD99069) dated 11/06/2001; 8 pages	<input type="checkbox"/>
18	USPTO Application Number 10/137,497 (CD01197M): "Reconfigurable Testing System and Method," Pleis et al.; filed on 05/01/2002; 40 pages	<input type="checkbox"/>
19	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 11/05/2004; 17 pages	<input type="checkbox"/>
20	USPTO Final Rejection for Application Number 10/137,497 (CD01197M) dated 05/05/2005; 13 pages	<input type="checkbox"/>
21	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 09/22/2005; 21 pages	<input type="checkbox"/>

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	22	USPTO Final Rejection for Application Number 10/137,497 (CD01197M) dated 03/13/2006; 15 pages	<input type="checkbox"/>
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	26	USPTO Application Number 10/653,050 (CD01242C): "Method and System for Programming a Memory Device," Snyder et al.; filed on 08/29/2003; 69 pages	<input type="checkbox"/>
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	28	USPTO Notice of Allowance for Application Number 10/653,050 (CD01242C) dated 07/29/2004; 3 pages	<input type="checkbox"/>
	29	USPTO Application Number 10/172,670 (CD01242M): "Method and System for Programming a Memory Device," Snyder et al.; filed on 06/13/2002; 66 pages	<input type="checkbox"/>
	30	USPTO Application Number 11/986,338 (CD1197C): Reconfigurable Testing System and Method," Pleis et al., filed on 11/20/2007; 41 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5952888	B1	1999-09-14	Scott	Entire Document
	2	5886582	B1	1999-03-23	Stansell	Entire Document
	3	5651035	B1	1997-07-22	Tozun	Entire Document
	4	5491458	B1	1996-02-13	McCune	Entire Document
	5	7295049	B1	2007-11-13	Moyal et al.	Entire Document
	6	5917356	B1	1999-06-29	Casal et al.	Entire Document
	7	4380083	B1	1983-04-12	Andersson et al.	Entire Document
	8	3600690	B1	1971-08-17	White, Colin Graham	Entire Document

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9	5631577	B1	1997-05-20	Freidin et al.	Entire Document
10	5587945	B1	1996-12-24	Lin et al.	Entire Document
11	5646901	B1	1997-07-08	Sharpe-Geisler et al.	Entire Document
12	6388464	B1	2002-05-14	Lacey et al.	Entire Document
13	6512395	B1	2003-01-28	Lacey et al.	Entire Document
14	5805897	B1	1998-09-08	Glowny, David Andrew	Entire Document
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17	6732347	B1	2004-05-04	Camilleri et al.	Entire Document
18	6686860	B1	2004-02-03	Gulati et al.	Entire Document
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20	5646544	B1	1997-07-08	Iadanza, Joseph Andrew	Entire Document
21	5384910	B1	1995-01-24	Torres, Robert J.	Entire Document
22	5479603	B1	1995-12-26	Stone et al.	Entire Document
23	5729704	B1	1998-03-17	Stone et al.	Entire Document
24	5974235	B1	1999-10-26	Nunally et al.	Entire Document
25	6658498	B1	2003-12-02	Carney et al.	Entire Document
26	6799198	B1	2004-09-28	Huboi et al.	Entire Document
27	7047166	B1	2006-05-16	Dancea, Ioan	Entire Document
28	6289478	B1	2001-09-11	Kitagaki, Takashi	Entire Document
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31	5430734	B1	1995-07-04	Gilson, Kent L.	Entire Document
32	6332137	B1	2001-12-18	Honi et al.	Entire Document
33	7020854	B1	2006-03-28	Killian et al.	Entire Document
34	6880086	B1	2005-04-12	Kidder et al.	Entire Document
35	7367017	B1	2008-04-29	Maddock et al.	Entire Document
36	6954904	B1	2005-10-11	White, Jason	Entire Document
37	7152027	B1	2006-12-19	Adreade et al.	Entire Document
38	6097211	B1	2000-08-01	Couts-Martin et al.	Entire Document

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1	20030105620		A1	2003-06-05	Bowen, Matt	Entire Document

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		Filing Date	2001-11-01
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		Attorney Docket Number	CD01213M

	2	20030097640	A1	2003-05-22	Abrams et al.	Entire Document
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	1	USPTO Application Number 11/273,708 (CD05141): "Capacitance Sensor Using Relaxation Oscillators," Snyder et al., filed on 11/14/2005; 33 pages	<input type="checkbox"/>
	2	USPTO Application Number 11/983,291 (CD05191D): "Successive Approximate Capacitance Measurement Circuit," Warren Snyder, filed on 11/07/2007; 26 pages	<input type="checkbox"/>
	3	USPTO Application Number 11/698,660 (CD05176): "Configurable Bus," Kutz et al., filed on 01/25/2007; 35 pages	<input type="checkbox"/>
	4	USPTO Application Number 11/709,866 (CD05170): "Input/Output Multiplexer Bus," Dennis Sequine, filed on 02/21/2007; 33 pages	<input type="checkbox"/>
	5	USPTO Application Number 11/337,272 (CD05191): "Successive Approximate Capacitance Measurement Circuit;" Warren Snyder, filed on 01/20/2006 29 pages	<input type="checkbox"/>

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Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

6	USPTO Application Number 11/166,622 (CD05027): "Touch wake for electronic devices," Beard et al., filed on 06/23/2005; 22 pages	<input type="checkbox"/>
7	Sedra et al., "Microelectronic Circuits," 3rd Edition, 1991, Oxford University Press, 02/05/2007; 20 pages	<input type="checkbox"/>
8	USPTO Notice of Allowance for Application Number 10/024,093 (CD99069C) dated 09/10/2002; 7 pages	<input type="checkbox"/>
9	USPTO Application Number 10/024,093 (CD99069C): "Configurable Memory for Programmable Logic Circuits," Lacey et al., filed on 12/18/2001; 25 pages	<input type="checkbox"/>
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	Examiner Name	Jason Scott Proctor
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	13	USPTO Requirement for Restriction/Election for Application Number 11/818,005 (CD01171C) dated 07/14/2009; 5 pages	<input type="checkbox"/>
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34	USPTO Non-Final Rejection for Application Number 11/166,622 (CD05027) dated 09/29/2009; 11 pages	<input type="checkbox"/>
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	24	USPTO Non-Final Rejection for Application Number 10/133,581 (CD01188) dated 08/12/2005; 12 pages	<input type="checkbox"/>
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	34	USPTO Non-Final Rejection for Application Number 10/133,581 (CD01188) dated 02/24/2006; 11 pages	<input type="checkbox"/>
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	Examiner Name		Jason Scott Proctor
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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	7332976	B1	2008-02-19	Brennan, Aaron	Entire Document

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Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20090066427	A1	2009-03-12	Brennan, Aaron	Entire Document

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	8	USPTO Notice of Allowance for Application Number 11/799,439 (CD00167MC2) dated 02/05/2010; 4 pages	<input type="checkbox"/>
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12	USPTO Final Rejection for Application Number 12/070,547 (CD04179C) dated 10/30/2009; 5 pages	<input type="checkbox"/>
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15	USPTO Notice of Allowance for Application Number 09/943,062 (CD00228) dated 06/29/2010; 4 pages	<input type="checkbox"/>
16	USPTO Final Rejection for Application Number 11/818,005 (CD01171C) dated 05/24/2010; 8 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	6963908	B1	2005-11-08	Lynch et al.	Entire Document
	2	6754723	B1	2004-06-22	Kato, Takeshi	Entire Document
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	4	6701508	B1	2004-03-02	Bartz et al.	Entire Document
	5	7079166	B1	2006-07-18	Hong, Jerry	Entire Document
	6	7178096	B1	2007-02-13	Rangan et al.	Entire Document
	7	6615167	B1	2003-09-02	Devins et al.	Entire Document
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9	6686860	B1	2004-02-03	Gulati et al.	Entire Document
10	6817005	B1	2004-11-09	Mason et al.	Entire Document
11	4701907	B1	1987-10-20	Collins, Arthur	Entire Document
12	4813013	B1	1989-03-14	Dunn, Robert	Entire Document
13	5887189	B1	1999-03-23	Birns et al.	Entire Document
14	6057705	B1	2000-05-02	Wojewoda et al.	Entire Document
15	6321369	B1	2001-11-20	Heile et al.	Entire Document
16	6449761	B1	2002-09-10	Greidinger et al.	Entire Document
17	6542845	B1	2003-04-01	Grucci et al.	Entire Document
18	6571373	B1	2003-05-27	Devins et al.	Entire Document
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20	4604363	B1	1986-08-05	Newhouse et al.	Entire Document
21	4876466	B1	1989-10-24	Kondou et al.	Entire Document
22	4879688	B1	1989-11-07	Turner et al.	Entire Document
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25	5371883	B1	1994-12-06	Gross et al.	Entire Document
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27	5430734	B1	1995-07-04	Gilson, Kent	Entire Document
28	5453904	B1	1995-09-26	Higashiyama et al.	Entire Document
29	6876941	B1	2005-04-05	Nightingale, Andrew	Entire Document
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31	5721931	B1	1998-02-24	Gephardt et al.	Entire Document
32	6834384	B1	2004-12-21	Fiorella II et al.	Entire Document
33	5822531	B1	1998-10-13	Gorczyca et al.	Entire Document
34	5909544	B1	1999-06-01	Anderson II et al.	Entire Document
35	5931959	B1	1999-08-03	Kwiat, Kevin	Entire Document
36	6535061	B1	2003-03-18	Darmawaskita et al.	Entire Document
37	6289478	B1	2001-09-11	Kitagaki, Takashi	Entire Document
38	6332137	B1	2001-12-18	Hori et al.	Entire Document
39	6526556	B1	2003-02-25	Stoica et al.	Entire Document
40	6449761	B1	2002-09-10	Greidinger et al.	Entire Document
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	42	7047166	B1	2006-05-16	Dancea, Ioan	Entire Document
	43	6966039	B1	2005-11-15	Bartz et al.	Entire Document
	44	6971004	B1	2005-11-29	Pleis et al.	Entire Document
	45	7010773	B1	2006-03-07	Bartz et al.	Entire Document
	46	7287112	B1	2007-10-23	Pleis et al.	Entire Document
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	48	7152027	B1	2006-12-19	Andrade et al.	Entire Document
	49	6198303	B1	2001-03-06	Rangasayee, Krishna	Entire Document
	50	6880086	B1	2005-04-12	Kidder et al.	Entire Document
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	53	4831546	B1	1989-05-16	Mitsuta et al.	Entire Document
	54	5225991	B1	1993-07-06	Dougherty, Dawn	Entire Document
	55	5930148	B1	1999-07-27	Bjorksten et al.	Entire Document
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	57	5594876	B1	1997-01-14	Getzlaff et al.	Entire Document
	58	6003107	B1	1999-12-14	Ranson et al.	Entire Document
	59	6002268	B1	1999-12-14	Sasaki et al.	Entire Document
	60	6055584	B1	2000-04-25	Bridges et al.	Entire Document
	61	6253250	B1	2001-06-26	Evans et al.	Entire Document
	62	6473825	B1	2002-10-29	Worley et al.	Entire Document
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64	7305510	B1	2007-12-04	Miller, William V.	Entire Document
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2	20060015862	A1	2006-01-19	Odom et al.	Entire Document	
3	20060031768	A1	2006-02-09	Shah et al.	Entire Document	
4	20020099863	A1	2002-07-25	Comeau et al.	Entire Document	
5	20040018711	A1	2004-01-29	Madurawe, Raminda	Entire Document	
6	20080294806	A1	2008-11-27	Swindle et al.	Entire Document	

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	33	M. Mooris Mano, "Computer System Architecture," 1982, Prentice-Hall, 2nd Edition, pp. 261-264 and 435-440; 14 pages	<input type="checkbox"/>
	34	Dirk Killat, "A One-Chip Solution for Electronic Ballasts in Fluorescent Lamps," Power Electronics, < http://powerelectronics.com/mag/power_onechip_solution_electronic/ >, dated 03/01/2004, accessed 09/13/2005; 4 pages	<input type="checkbox"/>
	35	Fred Eady, "PSoC 101," Circuit Cellar, August 2004, accessed 09/13/2005, < http://www.circuitcellar.com/library/print/0804/eady169/2.htm >; 4 pages	<input type="checkbox"/>
	36	USPTO Notice of Allowance for Application Number 10/803,030 (CD00167C1) dated 01/08/2007; 6 pages	<input type="checkbox"/>
	37	USPTO Non-Final Rejection for Application Number 10/803,030 (CD00167C1) dated 06/08/2005; 4 pages	<input type="checkbox"/>
	38	Application Number 12/058,586 (CD2007122): "System and Method for Monitoring a Target Device," Ogami et al., filed on 03/28/2008; 41 pages	<input type="checkbox"/>
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41	Application Number 12/058,569 (CD2007125): "Configuration of Programmable IC Design Elements," Best et al., filed on 03/28/2008; 19 pages	<input type="checkbox"/>
42	Application Number 12/057,149 (CD2007156): "Power Management Architecture, Method and Configuration System," Kenneth Ogami, filed on 03/27/2008; 34 pages	<input type="checkbox"/>
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45	The Written Opinion of the International Search Report for International Application Number PCT/US10/33626 (CD2009119PCT) mailed 06/24/2010; 5 pages	<input type="checkbox"/>
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47	John Mangino, "Using DMA with High Performance Peripherals to Maximize System Performance," 2007, Texas Instruments, pp. 1-23; 23 pages	<input type="checkbox"/>
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	Filing Date		2001-11-01
	First Named Inventor		Craig Nemecek
	Art Unit		2123
	Examiner Name		Jason Scott Proctor
	Attorney Docket Number		CD01213M

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	1	5935233	B1	1999-08-10	Jeddeloh, Joseph	Entire Document
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	3	5377333	B1	1994-12-27	Nakagoshi et al.	Entire Document
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	8	5500823	B1	1996-03-19	Martin et al.	Entire Document

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